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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/015,691	12/17/2001	Touru Terada	Q67592	8085

7590

06/29/2005

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EXAMINER
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TRAN, LEN

ART UNIT	PAPER NUMBER
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1725

DATE MAILED: 06/29/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

10/015,691

Applicant(s)

TERADA ET AL.

Examiner

Len Tran

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 05 April 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-3, 8 and 9 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-3, 8-9 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

## DETAILED ACTION

### *Claim Rejections - 35 USC § 103*

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

3. Claims 1- 3 and 8-9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Shibata et al (US 6,193,132), and further in view of Matsumoto et al (US 6,337,489).

Shibata et al disclose the apparatus for bonding comprising of a bonding tool (12), a moving mechanism for moving the bonding tool, a **chip recognition camera (14) disposed to be lower than a level of a substrate mounted surface of the substrate stage to thereby**

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**recognize the chip held by the bonding tool as shown in the figures (col. 6, lines 10-16).**

Shibata et al also disclose **a substrate recognition camera (16) disposed above the substrate stage to recognize the substrate mounted on the substrate stage**, and wherein the chip and substrate are subjected to positioning on the basis of recognition results of the chip recognition camera and the substrate recognition camera (figures 1, 5, 7, col. 4, lines 4-11, lines 25-37). In addition, Shibata inherently disclose a control device to control the bonding tool, since the bonding tool is moveable. Furthermore, the control device is also moving the substrate (11) as well as the recognition camera (14).

Shibata et al fail to disclose an up and down mechanism to control the bonding tool and a chip tray located lower than the bonding surface of the substrate.

However, Shibata et al disclose the bonding tool to move in a horizontal direction and the stage (10) move upward for bonding. Therefore, setting only the bonding tool to both move in the horizontal and vertical direction would have been obvious, since Shibata et al explicitly discloses such movement for bonding both chip and substrate.

However, Matsumoto et al is introduced to show the vertical movement of the bonding tool and the location of the chip tray to be lower than the level of the chip bonding surface of the substrate for the purpose of positioning the chip on the substrate with high precision.

Therefore, it would have been obvious to one of ordinary skill in the art at the time applicant's invention was made to provide a vertical mechanism for moving the bonding tool and a lower chip tray as taught by Matsumoto et al, in Shibata et al in order to position the chip to the substrate with high precision.

***Response to Arguments***

4. Applicant's arguments filed 4/05/05 have been fully considered but they are not persuasive.

As to applicant's argument regarding to the 112, 2<sup>nd</sup> paragraph rejection on pages 6-7, examiner withdrew the rejection.

As to pages 8-11, Applicant argues that Shibata fails to teach a bonding apparatus having *a chip recognition camera disposed to be lower than a level of a substrate mounting surface* of a substrate stage *to thereby recognize a chip held by a bonding tool from a position below the chip*, wherein a *control device has moved the chip so that the chip recognition camera recognizes a lower surface of the chip when the lower surface of the chip is located substantially on a level with a chip bonding surface of the substrate*. Examiner respectfully disagrees. Examiner had indicated earlier in the prosecution that the claimed language pertains to **an apparatus**. The apparatus of Shibata is capable of performing the claimed function, since it has all the claimed elements. Applicant further argues that examiner may not rely on measurements from Shibata's figures, since the drawings are not to scale. Examiner respectfully disagrees. Figures 1, 5, and 7 in Shibata is consistent with each other, showing the camera to be lower than the mounting surface for the substrate, in which the figures in Shibata is essentially the same as applicant's invention. Therefore, *it is obvious to one of ordinary skill in the art to understand that the camera is lower than the mounting surface. In addition, if the camera is*

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*above the mounting surface, then upon raising the table (10), the camera would be restricted.*

Therefore, it is obvious to an ordinary skill in the art to place the camera below the mounting surface. In conclusion, Shibata's drawing might not be shown in exact scale, however, it is obvious to one of ordinary skill in the art that Shibata's intention is to show the camera is below the mounting surface.

In page 10, applicant further argues that Shibata and Matusumoto fail to recognize the chip when the lower surface of the chip is located substantially on a level with a chip bonding surface of the substrate, to eliminate the shift amount. However, such argument is not in commensurate with the scope of the claim, since applicant does not claim the argued function. Therefore, claims 1-3 and new claims 8 and 9 are rejected.

### ***Conclusion***

**5. THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event,

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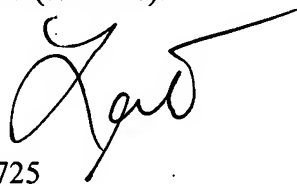
however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Len Tran whose telephone number is (571) 272-1184. The examiner can normally be reached on M-F, 8:30 - 5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Dunn can be reached on (571) 272-1171. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Len Tran  
Examiner  
Art Unit 1725



June 27, 2005